



**Department of Electronic Engineering
Faculty of Engineering and Technology**



Rubric for Final Year Project (FYP) Viva and Project Demonstration

PROJECT TITLE:					
Sr. No.	Student Name	Roll No.	Obtained Marks (for respective Criteria)		Total Marks (out of 25)
			I (18)	II (7)	
01					
02					

Sr. No.	Criteria	Evaluation Scale				Marks
		Excellent	Good	Just Acceptable	Unacceptable	
	Marks Range	13.6-18	9.1-13.5	4.6-9	0-4.5	
I	Prototype/Model Demonstration [CLO 4, PLO 1]	The developed hardware/model is excellently designed, meets research objective and is working accurately.	The developed hardware/ model moderately meets research objective and project was running with minor errors.	The hardware/ model lacks consideration of problem statement and demonstration showed several errors.	No hardware/ model implementation and demonstration showed	18
II	Marks Range	5.26-7	3.6-5.25	1.76-3.5	0-1.75	7
	Presentation [CLO 5, PLO 5 & 12]	Clear, systematic and precise demonstration of entire project work flow concepts and achieved results	Good understanding and familiarization of entire project work flow concepts and results in terms of demonstration	Unsystematic, uncertain and confused demonstration of project work flow concepts and achieved results	No and project concept and its associated part demonstrated	

Dated

Name and Signature: _____