

Curriculum Vitae

Engr.Fozia Hajano

Lecturer

Personal Information

Name	Engr.fozia Hajano
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Postal Address	H No A-30, near Bahria College, Gulistan-e-Sajjad, Qasimabad, Hyderabad, Pakistan.

Education

Degree	Major	Duration	Institute	CGPA/Grade
PhD	Electronic Engineering	2022 Present	Mehran UET, Jamshoro	-
Masters	Electronic Engineering	2013	Mehran UET, Jamshoro	3.88 CGPA
Bachelor	Electronic Engineering	2017	Mehran UET, Jamshoro	3.64 CGPA
HSC	Pre-Engineering	2013	Superior Collage	Grade A1
SSC	Science	2011	St. Bonaventure High School	Grade A

EXPERIENCE (Teaching)			
Position	Employer	From	To
Teaching Assistant	Department of Electronic Engineering, MUET Jamshoro	2018	2020
Lab Engineer	Department of Electronic Engineering, Faculty of Engineering and Technology, University of Sindh	2022	2023
Lecturer	Department of Electronic Engineering, Faculty of Engineering and Technology, University of Sindh	2023	Present

COMPUTER LITERACY

Programming Languages: C/C++, Python

Tools (e.g., MATLAB, MULTISIM)

PUBLICATIONS

1. **Hajano, F.**, Abro, F. R., Memon, T. D., Kalwar, I. H., & Burhan. "Fuzzy logic based anti-slip control of commuter train with FPGA implementation," *International Journal of Advanced Computer Science and Applications*, vol. 11, no. 4, pp. 293-300, 2020. doi:10.14569/IJACSA.2020.0110440
 2. S. Hajano, **F. Hajano**, S.Hajano "Deep Learning Approaches for Classifying EMG Signals in Amputees," *Mehran Univ. Res. J. Eng. Technol. (MURJET)*, ISSN 0254-7821, e-ISSN 2413-7219, under review.
- S. Hajano, S. Narejo, S. Hajano, and **F. Hajano**, "A Few-Shot Learning Framework for Understanding Mobile User Behaviour," *Mehran Univ. Res. J. Eng. Technol. (MURJET)*, ISSN 0254-7821, e-ISSN 2413-7219, under review.

PROJECTS

Worked on HOME SECURITY SYSTEM using Raspberry-Pi as a Final Year Bachelor's Project	2017
Fuzzy logic-based anti-slip control of commuter train with FPGA implementation	2020